IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Atty Dkt. 4074-9

C# M#

TAKENAKA

TC/A.U.

2822

Serial No. 10/694,807

Examiner: Guerrero, Maria

Filed: October 29, 2003

Date: February 7, 2005

Title:

METHOD OF SEMICONDUCTOR DEVICE FABRICATION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

RESPONSE/AMENDMENT/LETTER

This is a response/amendment/letter in the above-identified application and includes an attachment which is hereby incorporated by reference and the signature below serves as the signature to the attachment in the absence of any other signature thereon.

☐ Correspondence Address Indication Form Attached.			
Fees are attached as calculated below:			
Total effective claims after amendment	19	minus highest number	

previously paid for

previously paid for

20

(at least 20) =

x \$50.00

\$0.00 (1202)/\$0.00 (2202) \$

Independent claims after amendment

3

(at least 3) =

minus highest number x \$200.00 0

\$0.00 (1201)/\$0.00 (2201) \$

If proper multiple dependent claims now added for first time, (ignore improper); add

\$360.00 (1051)/\$180.00 (2051) \$

paper and attachment(s)

Petition is hereby made to extend the current due date so as to cover the filing date of this One Month Extension \$120.00 (1251)/\$60.00 (2251)

Two Month Extensions \$450.00 (1252)/\$225.00 (2252)

Three Month Extensions \$1020.00 (1253/\$510.00 (2253)

Four Month Extensions \$1590.00 (1254/\$795.00 (2254) \$

Terminal disclaimer enclosed, add

\$130.00 (1814)/\$65.00 (2814) \$

Applicant claims "small entity" status.

☐ Statement filed herewith

Rule 56 Information Disclosure Statement Filing Fee

\$180.00 (1806)

TOTAL FEE ENCLOSED \$

Assignment Recording Fee

\$40.00 (8021)

\$

Other:

JAR:caj

\$

0.00

\$

The Commissioner is hereby authorized to charge any deficiency, or credit any overpayment, in the fee(s) filed, or asserted to be filed, or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Account No. 14-1140. A duplicate copy of this sheet is attached.

1100 North Glebe Road, 8th Floor Arlington, Virginia 22201-4714 Telephone: (703) 816-4000 Facsimile: (703) 816-4100

Signature:

NIXON & VANDERHYE P.C. By Atty: Joseph A. Rhoa, Reg. 37,515

924062

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE FEB 0 7 2005 FEB 17 2005 FEB 17 2005

TAKENAKA

Atty. Ref.: 4074-9; Confirmation No.

Appl. No. 10/694,807

TC/A.U. 2822

Filed: October 29, 2003

Examiner: Guerrero, Maria

For: METHOD OF SEMICONDUCTOR DEVICE FABRICATION

* * * * * * * * * * *

Feb. 7, 2005

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

AMENDMENT

Responsive to the Official Action dated November 17, 2004, please amend the above-identified application as follows: